

Art Unit 2824
Serial No.10/816,271

Reply to Office Action of: 09/21/2005
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AMENDMENTS TO THE SPECIFICATION

In the Abstract:

Please delete the current Abstract and replace it in its entirety with the following text:

In a method and system for reducing power consumed by a magnetic memory, magnetic memory cells are coupled to a bit line and are associated with a plurality of digit lines. A bit line current is provided in the bit line. Digit currents are provided in parallel in the digit lines at substantially the same time as the bit line current. The digit and bit line currents allow the magnetic memory cells to be written to a plurality of states in parallel.